



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re U.S. Reissue Patent Application of NABIL N. GHALY

Filed: January 23, 1995

S.N.: 08/376,789

For: ELECTRONIC HAND HELD LOGIC GAME

Attorney's Docket: 0151-125P/JAB

Assistant Commissioner for Patents
Washington, D.C. 20231

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AMENDMENT TRANSMITTAL

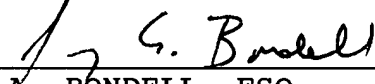
Sir:

Transmitted herewith is a Supplemental Amendment After Allowance in the above-entitled application.

Small entity status of this application under 37 C.F.R. 1.27 has been established by a verified statement previously submitted.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0748. A duplicate copy of this sheet is enclosed.

Any patent application processing fees under 37 C.F.R. 1.17.



JAY A. BONDELL, ESQ.,
Attorney For Applicant
REG. #28,188

Date July 15, 1997

SCHWEITZER CORNMAN GROSS & BONDELL LLP
230 Park Avenue
New York, New York 10169
Tel: (212) 986-3377 - Fax: (212) 986-6126

CERTIFICATE UNDER 37 C.F.R. 1.8(a)

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks Washington, D.C. 20231 on July 15, 1997.

Applicant's Attorney: 



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

: NABIL N. GHALY

For

: ELECTRONIC HAND HELD LOGIC GAME
Reissue application of U.S.
Patent No. 5,286,037
Issued February 15, 1994

Serial No.

: 08/376,789

Filed

: January 23, 1995

Attorney Docket

: 0151-125P/JAB

Examiner

: M. O'Neill,
Group Art Unit 3304

Hon. Assistant Commissioner
for Patents
Washington, D.C. 20231

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SUPPLEMENTAL AMENDMENT AFTER ALLOWANCE

Sir:

In response to the telephonic communication from the Examiner regarding outstanding matters in connection with the present reissue application, kindly enter the following.

In the Claims:

22. An electronic game device as recited in claim 1 wherein each of said plurality of routing means is depicted as a two-dimensional geometric [square] shape having four edges and comprises binary switching means and [further comprises eight (8) ports (]four input ports and four output ports[)] which are depicted to be located at the four (4) edges of the corresponding geometric [square] shape such that one input port and one output